

Intel® Media Software Development Kit 2014 HEVC Software Pack Release Notes

[Overview](#)

[New Features](#)

[System Requirements](#)

[Package Contents](#)

[Installation](#)

[How To Use](#)

[Known Limitations](#)

[Legal Information](#)

Overview

The **Intel® Media Software Development Kit (Intel Media SDK) HEVC Software Pack** is a set of software development libraries (plug-ins) and tools that expose the HEVC (ISO*/IEC* 23008-2 MPEG-H Part 2 or ITU-T* H.265 standard) decode and encode acceleration capabilities of Intel® platforms.

Plug-ins are not standalone but additional libraries expanding the Intel® Media SDK for Linux Servers with HEVC decode and encode functionality. The Intel® Video Pro Analyzer is a GUI tool for coded video bitstream analysis that allows to inspect each major step of decoding process visually and numerically.

New Features

Intel® Media SDK HEVC Software Pack is a new product that includes:

- *Intel® Media SDK HEVC Software Decode Plug-in*

Please see the Intel® Media SDK HEVC Software Decode Plug-in Release Notes for details:

`<install-folder>/plugins/mfxplugin-hevcd-release-notes.pdf`

- *Intel® Media SDK HEVC Software Encode plug-in*

Please see the Intel® Media SDK HEVC Software Encode Plug-in Release Notes for details:

`<install-folder>/plugins/mfxplugin-hevce-release-notes.pdf`

- *Intel® Video Pro Analyzer*

Please see the Intel® Video Pro Analyzer Release Notes for details:

`<install-folder>/tools/VideoProAnalyzerEvaluation/
release_notes.rtf`

System Requirements

Hardware

- For plug-in libraries: Intel® 64 architecture processors with support for Supplemental Streaming SIMD Extensions 3 instructions.

Software

- Please follow requirements Intel® Media SDK 2014 for Linux Servers Release Notes document.

Package Contents

Note: <install-folder> - folder where Intel® Media SDK HEVC Software Pack is installed.

<install-folder>	Intel® Media SDK HEVC Software Pack Release Notes (this file), End User License Agreement (EULA) "Intel_Media_SDK_HEVC_Software_Pack_EULA.pdf", a license file <code>license.txt</code>
<install-folder>/plugins	Contains Intel® Media SDK HEVC Software Decode and Encode plug-ins. Please see the corresponding Release Notes for details.
<install-folder>/samples	Contains binaries and documentation for the following samples: <ul style="list-style-type: none">• Video Decoding Sample This sample performs decoding of elementary compressed video stream to raw frames; it supports decoding of HEVC (High Efficiency Video Coding) video via Intel® Media SDK HEVC Software Decode Plug-in• Video Encoding Sample This sample performs encoding of raw video frames into elementary compressed stream; it supports encoding HEVC video via Intel Media SDK HEVC Software Encode Plug-in• Video Transcoding Sample This sample performs transcoding of elementary video stream from one compressed format to another; it supports decoding and encoding of HEVC (High Efficiency Video Coding) video via Intel® Media SDK HEVC Software Decode and Encode Plug-ins.

	Note: These samples are part of Intel® MediaSDK 2014 for Linux Servers and not distributed separately.
<code><install-folder>/tools/</code>	<p>Contains the following tools in binary form:</p> <ul style="list-style-type: none"> Intel® Video Pro Analyzer in folder <code>VideoProAnalyzerEvaluation</code>. <p>Please see the corresponding Release Notes for details.</p>

Installation

- Installation requires full administrative rights.
- Intel® Media SDK HEVC Software Pack requires Intel® Media SDK 2014 for Linux Servers. The default top-level installation folder for Intel® Media SDK 2014 for Linux Servers is:

```
/opt/intel/mediasdk
```

- Unpack `Intel_HEVC_Pack_x64-<version>.tgz`
 - Enter to `Intel_HEVC_Pack_x64-<version>` folder and run there installer there
- ```
./install.sh
```

Installer will guide installation process, please follow it.

- The default top-level installation folder for Intel® Media SDK HEVC Software Pack is:

```
/opt/intel/mediasdk/plugins
```

## How To Use

You can find more information on how to use Intel® Media SDK HEVC Software Pack together with Intel Media SDK 2014 for Linux Servers in the following documentation:

- `<MediaSDK2014-install-folder>/doc/mediasdk-man.pdf`  
"Intel Media SDK Reference Manual" describes the Intel Media SDK API, including HEVC codec related definitions.
- `<MediaSDK2014-install-folder>/doc/mediasdkusr-man.pdf`  
"Intel Media SDK Extensions for User-Defined Functions" describes an API extension (aka plug-ins API) that allows seamless integration of user-defined functions in Intel Media SDK pipelines.
- `<MediaSDK2014-install-folder>/doc/Intel_Media_Developers_Guide.pdf`

“Intel Media SDK 2014 Developer’s Guide” provides development hints and tips to help developers create media applications using Intel Media SDK. It also covers specifics of plug-ins API usage.

**Note:** <MediaSDK2014-install-folder> - folder where Intel Media SDK 2014 for Linux Servers is installed.

## Known Limitations

The Intel® Media SDK HEVC Software Pack has the following known limitations:

- The Intel Video Pro Analyzer is an evaluation copy. It is fully functional with the following limitations:
  - Picture size limited to 1920x1088. Larger streams will not open.
  - Picture count limited to 4. Only the first 4 pictures will load if a sequence contains more.

Please see Release Notes for each component for more details.

## Legal Information

INFORMATION IN THIS DOCUMENT IS PROVIDED IN CONNECTION WITH INTEL PRODUCTS. NO LICENSE, EXPRESS OR IMPLIED, BY ESTOPPEL OR OTHERWISE, TO ANY INTELLECTUAL PROPERTY RIGHTS IS GRANTED BY THIS DOCUMENT. EXCEPT AS PROVIDED IN INTEL'S TERMS AND CONDITIONS OF SALE FOR SUCH PRODUCTS, INTEL ASSUMES NO LIABILITY WHATSOEVER AND INTEL DISCLAIMS ANY EXPRESS OR IMPLIED WARRANTY, RELATING TO SALE AND/OR USE OF INTEL PRODUCTS INCLUDING LIABILITY OR WARRANTIES RELATING TO FITNESS FOR A PARTICULAR PURPOSE, MERCHANTABILITY, OR INFRINGEMENT OF ANY PATENT, COPYRIGHT OR OTHER INTELLECTUAL PROPERTY RIGHT.

UNLESS OTHERWISE AGREED IN WRITING BY INTEL, THE INTEL PRODUCTS ARE NOT DESIGNED NOR INTENDED FOR ANY APPLICATION IN WHICH THE FAILURE OF THE INTEL PRODUCT COULD CREATE A SITUATION WHERE PERSONAL INJURY OR DEATH MAY OCCUR.

Intel may make changes to specifications and product descriptions at any time, without notice. Designers must not rely on the absence or characteristics of any features or instructions marked "reserved" or "undefined." Intel reserves these for future definition and shall have no responsibility whatsoever for conflicts or incompatibilities arising from future changes to them. The information here is subject to change without notice. Do not finalize a design with this information.

The products described in this document may contain design defects or errors known as errata which may cause the product to deviate from published specifications. Current characterized errata are available on request.

Contact your local Intel sales office or your distributor to obtain the latest specifications and before placing your product order.

Copies of documents which have an order number and are referenced in this document, or other Intel literature, may be obtained by calling 1-800-548-4725, or by visiting [Intel's Web Site](#).

MPEG is an international standard for video compression/decompression promoted by ISO. Implementations of MPEG CODECs, or MPEG enabled platforms may require licenses from various entities, including Intel Corporation.

Intel, the Intel logo, Intel Core are trademarks or registered trademarks of Intel Corporation or its subsidiaries in the United States and other countries.

### **Optimization Notice**

Intel's compilers may or may not optimize to the same degree for non-Intel microprocessors for optimizations that are not unique to Intel microprocessors. These optimizations include SSE2, SSE3, and SSE3 instruction sets and other optimizations. Intel does not guarantee the availability, functionality, or effectiveness of any optimization on microprocessors not manufactured by Intel.

Microprocessor-dependent optimizations in this product are intended for use with Intel microprocessors. Certain optimizations not specific to Intel microarchitecture are reserved for Intel microprocessors. Please refer to the applicable product User and Reference Guides for more information regarding the specific instruction sets covered by this notice.

Notice revision #20110804