

Intel® Parallel Studio XE 2015 Professional Edition for Windows* Installation Guide and Release Notes

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Table of Contents

1	Introduction	1
1.1	What's New	2
1.1.1	Changes from Intel Parallel Studio XE 2013 SP1	2
1.2	Product Contents	2
1.3	System Requirements	3
1.3.1	Visual Studio 2008* is Not Supported	4
1.3.2	Windows XP* is Not Supported	4
1.4	Documentation	4
1.5	Technical Support	4
2	Installation	4
2.1	Online Installer	5
2.2	Using a License Server	5
3	Disclaimers, Notices and Legal Information	5

1 Introduction

This document describes system requirements and how to install Intel® Parallel Studio XE 2015 Professional Edition for Windows*. Additional release notes for each component, with details of changes and additional technical information, can be found after installation, in the respective components' `Documentation` folder.

First-time users should view the Getting Started page that is displayed at the end of installation.

1.1 What's New

This section highlights important changes from the previous product version and changes in product updates. For information on what is new in each component, please read the individual component release notes.

Update 5 – September 2015

- Component products updated to current versions

Update 4 – May 2015

- Component products updated to current versions

Update 3 – April 2015

- Component products updated to current versions

Update 2 – February 2015

- Component products updated to current versions

Update 1 – November 2014

- Component products updated to current versions

1.1.1 Changes from Intel Parallel Studio XE 2013 SP1

- Components updated to current versions
- Support for Static Analysis is deprecated and will be removed in a future release. If you have concerns or feedback, [please comment](#).
- Windows XP* not supported
- Microsoft Visual Studio 2008* not supported

1.2 Product Contents

Intel® Parallel Studio XE 2015 Professional Edition for Windows Update 5* includes the following components:

- Intel® Parallel Studio XE 2015 Composer Edition Update 5 - includes Intel® Visual Fortran Compiler, Intel® C++ Compiler, Intel® Integrated Performance Primitives (Intel® IPP), Intel® Threading Building Blocks (Intel® TBB) and Intel® Math Kernel Library (Intel® MKL)
- Intel® Advisor XE 2015 Update 1
- Intel® Inspector XE 2015 Update 1
- Intel® VTune™ Amplifier XE 2015 Update 4

1.3 System Requirements

For an explanation of architecture names, see [Intel® architecture platform terminology](#)

- A PC based on an IA-32 or Intel® 64 architecture processor supporting the Intel® Streaming SIMD Extensions 2 (Intel® SSE2) instructions (Intel® Pentium® 4 processor or later, or compatible non-Intel processor)
 - Incompatible or proprietary instructions in non-Intel processors may cause the analysis capabilities of this product to function incorrectly. Any attempt to analyze code not supported by Intel® processors may lead to failures in this product.
 - For the best experience, a multi-core or multi-processor system is recommended
- 2GB RAM (4GB recommended)
- 8GB free disk space for all product features and architectures
- For Intel® Many Integrated Core Architecture (Intel® MIC Architecture) development/testing:
 - Intel® Xeon Phi™ processor
 - Intel® Manycore Platform Software Stack (Intel® MPSS)
 - Debugging of offload code requires Microsoft Visual Studio* 2012 or 2013
- Microsoft Windows 7*, Microsoft Windows 8*, Microsoft Windows 8.1*, Microsoft Windows Server 2012* or Microsoft Windows Server 2008*; 32-bit or “x64” editions - embedded editions not supported
- One or more of:
 - Microsoft Visual Studio Professional 2013* (or higher edition) with C++ component installed
 - Microsoft Visual Studio Professional 2012* (or higher edition) with C++ component installed
 - Microsoft Visual Studio 2010* Professional Edition (or higher edition) with C++ and “x64 Compiler and Tools” components installed
 - For Intel® Visual Fortran, Intel® Advisor XE, Intel® Inspector XE and Intel® VTune™ Amplifier XE use only, Microsoft Visual Studio 2010* Shell and Libraries from the Intel® Visual Fortran Composer XE installation
- For development and debugging of applications using Intel® Xeon Phi™ coprocessors:
 - A system with an Intel® Xeon Phi™ coprocessor
 - Microsoft Visual Studio 2012 Professional Edition (or higher edition) with C++ component installed
 - Intel® Manycore Platform Software Stack

Notes:

1. The default for the Intel® compilers is to build IA-32 architecture applications that require a processor supporting the Intel® SSE2 instructions - for example, the Intel® Pentium® 4 processor. A compiler option is available to generate code that will run on any IA-32 architecture processor. However, if your application uses Intel® Math Kernel Library, Intel® Integrated Performance Primitives or Intel® Threading Building Blocks, executing the application will require a processor supporting the Intel® SSE2 instructions.

2. Applications built with Intel® Compilers can be run on the same Windows versions as specified above for development. Applications may also run on non-embedded 32-bit versions of Microsoft Windows earlier than Windows 7, though Intel does not test these for compatibility. Your application may depend on a Win32 API routine not present in older versions of Windows. You are responsible for testing application compatibility. You may need to copy certain run-time DLLs onto the target system to run your application.
3. OpenMP* applications to be analyzed by Intel® Inspector XE or Intel® VTune™ Amplifier XE and are built with the Microsoft compiler must link to the OpenMP “compatibility library” as supplied by an Intel® compiler.
4. Deprecated features may be removed in future versions.

1.3.1 Visual Studio 2008* is Not Supported

Support has been removed for installation and use with Visual Studio 2008. Intel recommends migrating to a newer version of Visual Studio*.

1.3.2 Windows XP* is Not Supported

Support has been removed for installation and use on Windows XP. Intel recommends migrating to a newer version of these operating systems.

1.4 Documentation

Product documentation can be accessed through the Help menu in Microsoft Visual Studio. It can also be found, along with “Getting Started” information, in the Windows “Start” menu under Intel Parallel Studio XE 2015. Please note that if you view the documentation in Microsoft Internet Explorer*, the browser may display a security warning when you click on links to open a documentation set. If you see this warning, you should click the option to proceed.

1.5 Technical Support

If you did not register your compiler during installation, please do so at the [Intel® Software Development Products registration center](#). Registration entitles you to free technical support, product updates and upgrades for the duration of the support term.

For information about how to find Technical Support, Product Updates, User Forums, FAQs, tips and tricks, and other support information, please visit

<http://www.intel.com/software/products/support>

Note: If your distributor provides technical support for this product, please contact them for support rather than Intel.

2 Installation

The installation of the product requires a valid license file or serial number.

After downloading your product, double-click on the executable file (.EXE) to begin installation.

You do not need to uninstall previous versions before installing a newer version – the new version will coexist with the older versions. However, it should be noted that when installing

updates to a major release Intel Parallel Studio XE common files as well as the product components Intel Advisor XE, Intel Inspector XE, and Intel VTune Amplifier XE belonging to that major release will be updated.

If you want to remove older versions, you may do so before or after installing the newer one.

2.1 Online Installer

The default electronic installation package now consists of a smaller installation package that dynamically downloads and then installs packages selected to be installed. This requires a working internet connection and potentially a proxy setting if you are behind an internet proxy. Full packages are provided alongside where you download this online install package if a working internet connection is not available.

2.2 Using a License Server

If you have purchased a “floating” license, see Licensing: [Setting Up the Client for a Floating License](#). This article also provides a source for the Intel® License Manager for FLEXlm* product that can be installed on any of a wide variety of systems.

3 Disclaimers, Notices and Legal Information

Optimization Notice

Intel’s compilers may or may not optimize to the same degree for non-Intel microprocessors for optimizations that are not unique to Intel microprocessors. These optimizations include SSE2, SSE3, and SSSE3 instruction sets and other optimizations. Intel does not guarantee the availability, functionality, or effectiveness of any optimization on microprocessors not manufactured by Intel. Microprocessor-dependent optimizations in this product are intended for use with Intel microprocessors. Certain optimizations not specific to Intel microarchitecture are reserved for Intel microprocessors. Please refer to the applicable product User and Reference Guides for more information regarding the specific instruction sets covered by this notice.

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